

**Amendments to the Specification**

Please amend the paragraph beginning on line 17 of page 1 as follows:

As shown in Figure 1, a switching device 200 performs the function of IPC(Inter Processor Communication) between processors 100-1 through 100-n. An U-LINK interface cable for supporting the full-duplex transmission mode is connected between the switching device 200 and the processes 100-1 though 100-n. The switching device 200 is formed in such a manner that a number of ~~nodes~~ nodes 230A1-230An and 230B1-230Bn are connected to one D-BUS(switching unit) 210. In addition, each node 230A1-230An and 230B1-230Bn is connected to its respective processor 100-1 through 100-n. And, one processor is connected with 2 nodes.

Please amend the paragraph beginning on line 23 of page 2 as follows:

As shown therein, the node includes a duplexing control unit 234 for controlling redundancy duplexing; a node control unit 233 operated according to the control of the duplexing control unit 234 and for performing message relay function by interfacing by means of a processor and a U-LPK and interfacing ~~be~~ by means of the switching unit 210 and a D-BUS, receiving buffer (RX buffer) 232 for buffering a message to be transmitted from the processor to the switching unit 210 according to the control of the node control unit 233, and a transmission buffer (TX buffer) 231 for buffering a message to be transmitted from the switching unit 210 to the processor according to the control of the node control unit 233.